



Patent

Attorney's Docket No. H1419

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Bin Yu et al.) Group Art Unit: 2826
Application No.: 10/633,504) Examiner: F. Abraham
Filed: August 5, 2003)
For: VARYING CARRIER MOBILITY)
IN SEMICONDUCTOR DEVICES)
TO ACHIEVE OVERALL DESIGN)
GOALS)

INFORMATION DISCLOSURE STATEMENT
TRANSMITTAL LETTER

U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Issue Fee
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

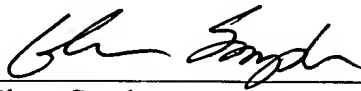
Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

- ☐ No additional fee for submission of the IDS is required.
- ☒ The fee of \$180.00 as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- ☒ A certification under 37 C.F.R. § 1.97(e) is also enclosed.
- ☐ Charge \$ _____ to Deposit Account No. 50-1070 for the fee due.
- ☒ A check in the amount of \$ 180.00 is enclosed for the fee due.

The Commissioner is hereby authorized to charge any other appropriate fees that may be required by this paper that are not accounted for above, and to credit any overpayment, to Deposit Account No. 50-1070.

Respectfully submitted,

HARRITY & SNYDER, L.L.P.

By: 
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CUSTOMER NUMBER: 45114

Date: April 12, 2005



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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(d)

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Alexandria, VA 22314

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(d), applicant(s) bring(s) to the attention of the Examiner the documents listed on the attached PTO 1449. This Information Disclosure Statement is being filed after a: ☐ Final Action; or ☒ Notice of Allowance, but before payment of the issue fee - and is accompanied by the certification as specified under § 1.97(e). Applicant(s) respectfully request(s) that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached PTO 1449 form.

04/14/2005 MBEYENE1 00000030 10633504

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☒ **Certification 1:** Each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

☐ **Certification 2:** No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.

☐ Copies of the listed documents are attached.

☐ Copies of the listed documents were previously submitted in a prior application, serial no. _____, filing date _____, upon which applicant(s) rely(ies) for the benefits provided in 35 U.S.C. § 120.

☐ The following is a concise statement of relevance of the non-English language documents.

1. _____ discloses _____.

2. _____ discloses _____.

☐ English translations of the non-English documents are enclosed.

☐ In lieu of a statement of relevance or translation of the non-English documents, an English language version of a search report from the _____ Patent Office in a corresponding application citing these documents and setting forth the relevance thereof is enclosed.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claims in the application and applicant(s) determine(s) that the cited document(s) do not constitute "prior art" under United States law, applicant(s) reserve(s) the right to present to the office the relevant facts and law regarding the appropriate status of such documents.

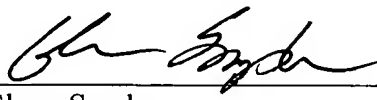
Applicant(s) further reserve(s) the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If any copending application(s) is/are cited on the attached PTO 1449, the Examiner's attention is directed to the foregoing application(s) in compliance with § 2001.06(b) of the Manual of Patent Examining Procedure. By identifying the copending application(s), the assignee and/or applicant of the application(s) do not waive confidentiality of the application(s). Accordingly, the U.S. Patent and Trademark Office is requested to maintain the confidentiality of the copending application(s) under 35 U.S.C. § 122.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 50-1070.

Respectfully submitted,

HARRITY & SNYDER, L.L.P.

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Glenn Snyder
Reg. No. 41,428

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Suite 300
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INFORMATION DISCLOSURE CITATION PTO-1449	CUSTOMER NUMBER 45114	ATTORNEY'S DKT No. H1419	APPLICATION No. 10/633,504	
		APPLICANT(S) Bin Yu et al.		
		FILING DATE August 5, 2003	GROUP 2826	

U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	2002/0011612A1	01/31/02	Hieda	257	262	
	2003/0102497A1	06/05/03	Fried et al.	257	255	
	2004/0110331A1	06/10/04	Yeo et al.	438	199	
	5,391,506	02/21/95	Tada et al.	437	41	

FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
	F. Dugé et al., "Channels Separation in FINFETs", ULIS'2003 Workshop, Udine, March 20-21 2003, 20 pages, XP002321769
	N.H.E. Weste et al., "Principles of CMOS VLSI Design", Addison-Wesley, XP002321770, pages 140-141, 1988
	J. Park et al., "Pi-Gate SOI MOSFET", IEEE Electron Device Letters, Vol. 22, No. 8, August 2001, pages 405-406, XP001099966
	M. Lemme et al., "Influence of channel width on n- and p-type nano-wire-MOSFETs on silicon on insulator substrate", Microelectronic Engineering, Vol. 67-68, June 2003, pages 810-817, XP004428954

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).